

The PhD Forum of VLSI-SoC 2018

October 8-10, 2018
Verona, Italy
Roseo Hotel Leon d'Oro



The **Ph.D. Forum of VLSI-SoC 2018** is a poster session aimed at the exchange of ideas and experiences of Ph.D. students from different parts of the world. Elected Ph.D. students have an opportunity to discuss their thesis and research work with specialists within the system and design automation communities. This offers a good opportunity for students to receive valuable feedback and gain exposure in the job market. Furthermore, this forum also provides a great chance for industry officials to meet junior researchers, giving an avenue for incorporating the latest research developments into their companies. **VLSI-SoC 2018** is the 26th in a series of international conferences sponsored by IFIP TC10 WG 5, IEEE CEDA and IEEE CASS. The theme this year is **"Design and engineering of electronics systems based on new computing paradigms"**. VLSI-SoC 2018 will be held in Verona, Italy. Verona, which has been awarded World Heritage Site status by UNESCO because of its urban structure and architecture, is the set of the Shakespeare's Romeo and Juliet play, one of the most visited Roman empire cities in northern Italy, the capital of the opera lyrics with its Arena roman amphitheater, and the land of the Amarone della Valpolicella vineyards.

Eligibility: The author must have completed at least one year of a Ph.D. program.

Presentation: Posters will be introduced in the PhD Forum Session (two minute time slot, one slide) and next presented in a full one-hour Poster Session.

Publication: Two (2) page abstracts will be provided to the conference attendees in soft proceedings (will NOT be included in the main conference proceedings).

Best PhD Forum Poster Award: The PhD forum selection committee will review all presented posters and recommend one poster for the Best PhD Forum Poster Award. We will have awards for second and third places as well.

Grants for PhD students: VLSI-SoC 2018 finances a limited number of travel grants for Ph.D. students, through the Technical Committee TC-10 (Computer Systems Technology) and WG 10.5 of the International Federation for Information Processing - IFIP. In principle, grants are restricted to support students who cannot be fully supported by their institutions. This rule gives preference to students being enrolled in Africa, Latin America, South America, Eastern Europe, and Asia (except Israel, Japan, Singapore, South Korea, Taiwan, and China).

How to apply: Submit a two-page abstract of the dissertation in PDF to kchoi@snu.ac.kr by July 2, 2018

PhD School: Students submitting their work to the PhD forum are encouraged to attend the PhD School that will be held on Friday 5, Saturday 6, and Sunday 7, co-located with VLSI-SoC.



Topics of interest include, but are not limited to:

- Analog, mixed-signal, and sensor architectures
- Digital architectures: NoC, multi- and many-core, hybrid, and reconfigurable
- CAD: Synthesis and analysis
- Prototyping, verification, modeling, and simulation
- Circuits and systems for signal processing and communications
- IoT, Embedded and Cyberphysical Systems: Architecture, design, and software
- Low-power and thermal-aware IC design
- Emerging technologies and computing paradigms
- Variability, reliability, and test
- Hardware security
- Machine learning for SoC design and for electronic design automation



General co-chairs:

Graziano Pravadelli (University of Verona, IT)
Todd Austin (University of Michigan, USA)

Program co-chairs:

Nicola Bombieri (University of Verona, IT)
Masahiro Fujita (University of Tokyo, JP)

Special session chairs:

Srinivas Katkoo (University of South Florida, USA)
Katell Morin-Allory (TIMA Laboratory, FR)

PhD forum chairs:

Kiyoung Choi (Seoul National University, KOR)
Sara Vinco (Politecnico di Torino, IT)

Industrial chair:

Yervant Zorian (Synopsys, USA)

Local chair:

Franco Fummi (University of Verona, IT)

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